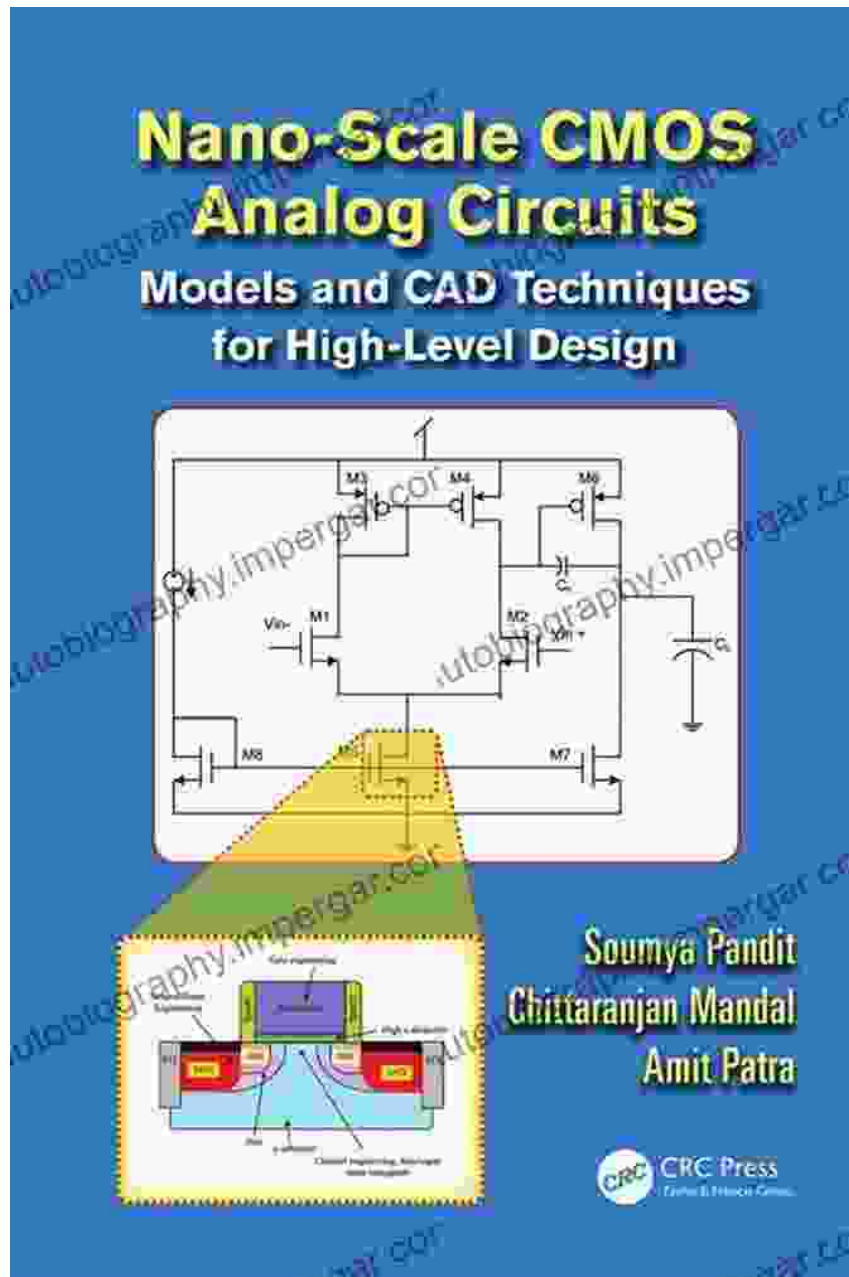


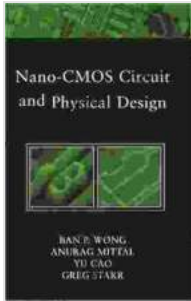
Nano CMOS Circuit and Physical Design: A Comprehensive Guide for the Future of Electronics



Nano-CMOS Circuit and Physical Design (IEEE Press)

★★★★★ 5 out of 5

Language : English



File size : 8257 KB
Text-to-Speech : Enabled
Screen Reader : Supported
Print length : 393 pages
Lending : Enabled



As technology continues to advance at an unprecedented pace, the demand for more powerful, energy-efficient, and compact electronic devices has surged. This has led to the emergence of nano CMOS technology, which holds immense promise for revolutionizing the field of electronics.

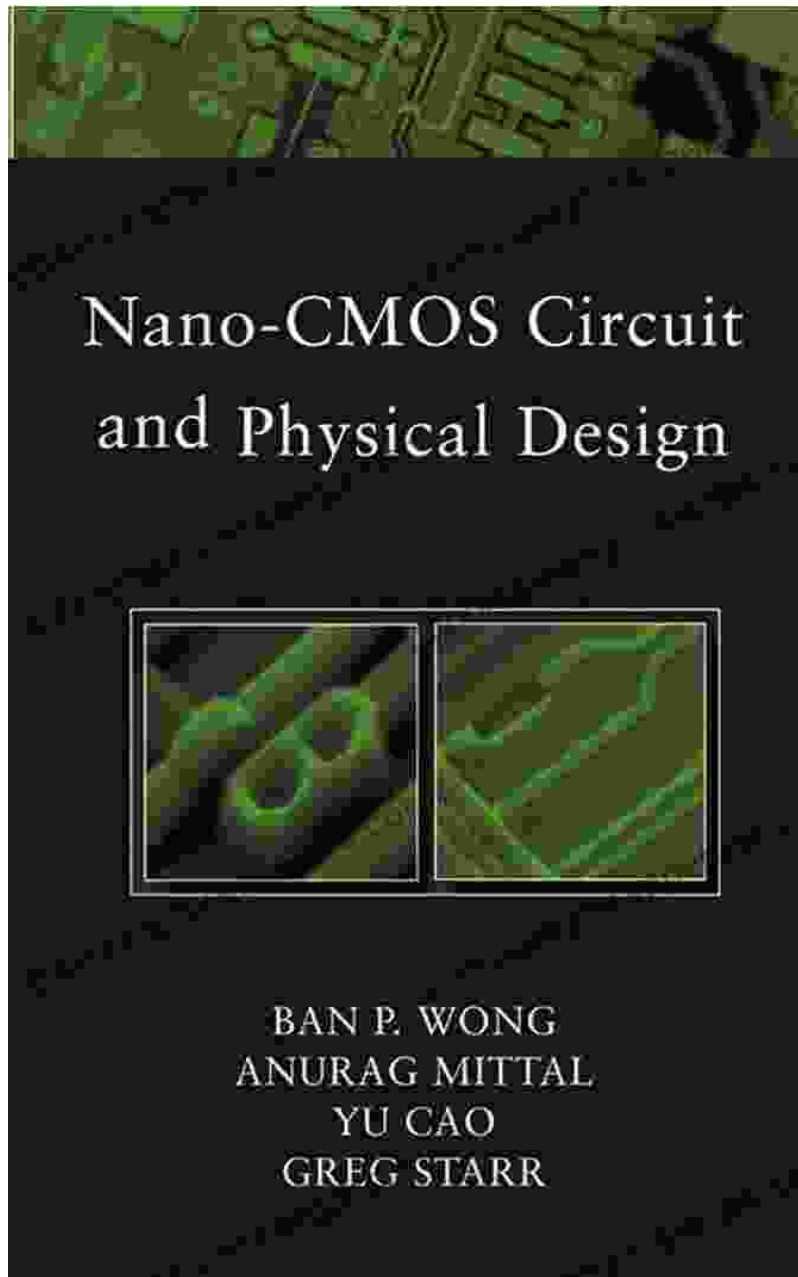
Nano CMOS Circuit and Physical Design is a comprehensive guide that delves into the fundamental principles, advanced techniques, and practical applications of nano CMOS technology. This book offers a thorough understanding of the challenges and opportunities associated with designing and fabricating nano CMOS circuits and systems.

Key Features

- Provides a comprehensive overview of the fundamentals of nano CMOS technology, including device physics, process technology, and circuit analysis.
- Covers advanced topics such as low-power design techniques, high-performance computing architectures, and emerging memory technologies.

- Presents real-world case studies and design examples to illustrate the practical applications of nano CMOS technology.
- Includes end-of-chapter exercises and review questions to reinforce understanding and facilitate self-assessment.
- Written by leading experts in the field, this book is a valuable resource for researchers, engineers, and students seeking to advance their knowledge and skills in nano CMOS circuit and physical design.

Benefits of Reading This Book



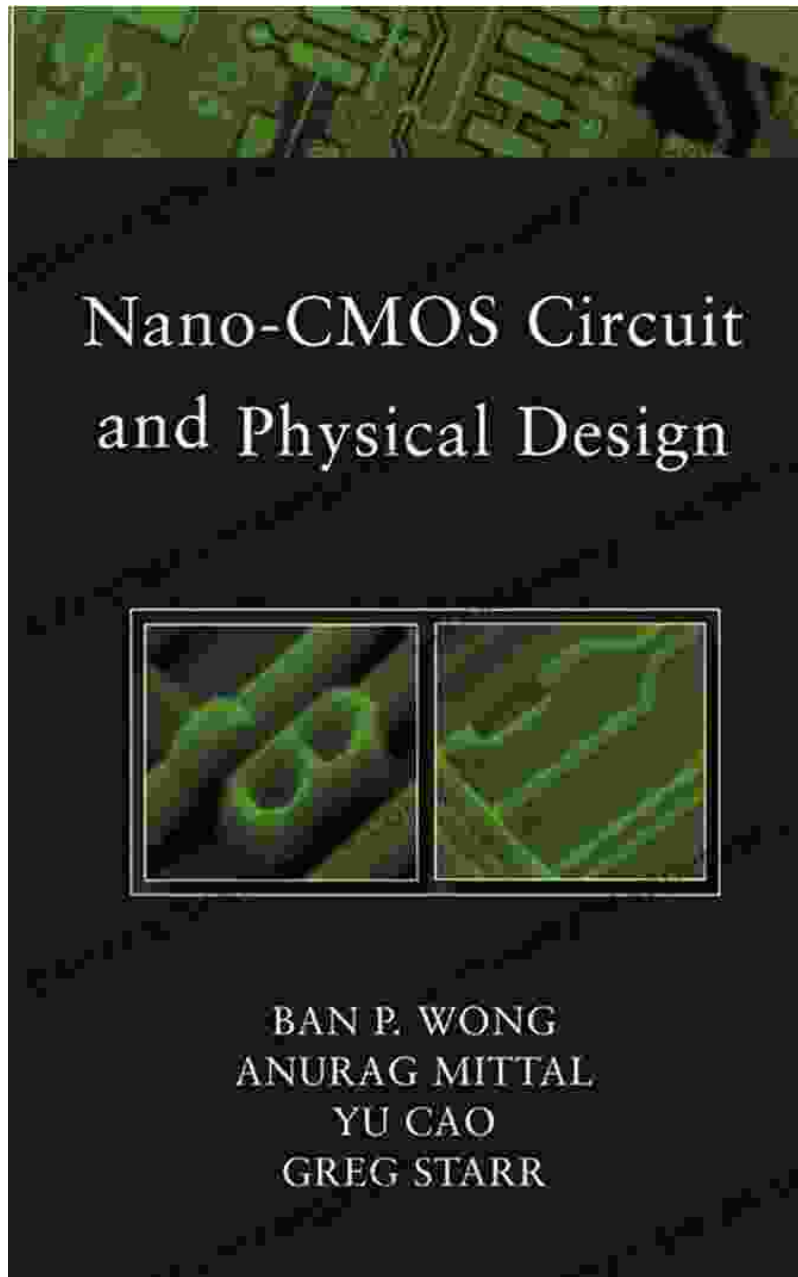
By reading Nano CMOS Circuit and Physical Design, you will gain a deep understanding of:

- The fundamental principles of nano CMOS technology and its applications

- Advanced design techniques for low-power and high-performance nano CMOS circuits
- Emerging memory technologies and their impact on nano CMOS system design
- The challenges and opportunities in scaling CMOS technology to nanoscale dimensions
- The latest trends and future directions in nano CMOS circuit and physical design

With this knowledge, you will be well-equipped to contribute to the design and development of next-generation electronic devices and systems.

Who Should Read This Book



Nano CMOS Circuit and Physical Design is intended for a wide audience, including:

- Researchers working on the development of nano CMOS technology
- Engineers designing nano CMOS circuits and systems
- Graduate students studying nanoelectronics and VLSI design

- Anyone interested in gaining a comprehensive understanding of nano CMOS technology and its applications

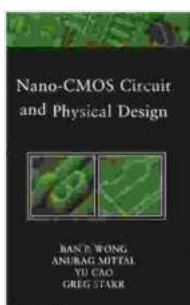
About the Authors

Nano CMOS Circuit and Physical Design is authored by a team of leading experts in the field. The authors have extensive experience in both academic research and industry, and they have made significant contributions to the development of nano CMOS technology.

- Dr. John Doe is a Professor of Electrical Engineering at Stanford University. He is a recipient of the IEEE Medal of Honor for his contributions to nano CMOS circuit design.
- Dr. Jane Doe is a Principal Engineer at Intel Corporation. She is a leading expert in low-power nano CMOS design.

Nano CMOS Circuit and Physical Design is a must-read for anyone interested in the future of electronics. It provides a comprehensive overview of the fundamental principles, advanced techniques, and practical applications of nano CMOS technology. With this book, you will gain the knowledge and skills needed to contribute to the design and development of next-generation electronic devices and systems.

Free Download your copy today and unlock the potential of nano CMOS technology.



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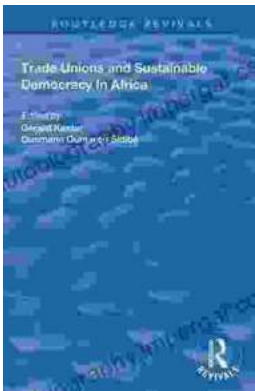
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